**2019-05-29 15:46 Project=C:\Autotesting\SIMCA\Created Projects\MergeRegular.usp**

**Totals:**

Passed 100.00%

Failed 0.00%

**Project changed to 'C:\Autotesting\SIMCA\Created Projects\MergeRegular.usp'**

**Development comparison applied with error margin '5000'**

**VarDS Comp1 File=MergeCase01.dif**

line=3

Passed 100.00%

Failed 0.00%

**VarDS Comp2 File=MergeCase02.dif**

line=4

Passed 100.00%

Failed 0.00%

**VarDS Comp3 File=MergeCase03.dif**

line=5

Passed 100.00%

Failed 0.00%

**VarDS Comp4 File=MergeCase04.dif**

line=6

Passed 100.00%

Failed 0.00%

**VarDS Comp5 File=MergeCase05.dif**

line=7

Passed 100.00%

Failed 0.00%

**VarDS Comp6 File=MergeCase06.dif**

line=8

Passed 100.00%

Failed 0.00%

**VarDS Comp7 File=MergeCase07.dif**

line=9

Passed 100.00%

Failed 0.00%

**VarDS Comp8 File=MergeCase08.dif**

line=10

Passed 100.00%

Failed 0.00%

**VarDS Comp9 File=MergeCase09.dif**

line=11

Passed 100.00%

Failed 0.00%

**VarDS Comp10 File=MergeCase10.dif**

line=12

Passed 100.00%

Failed 0.00%